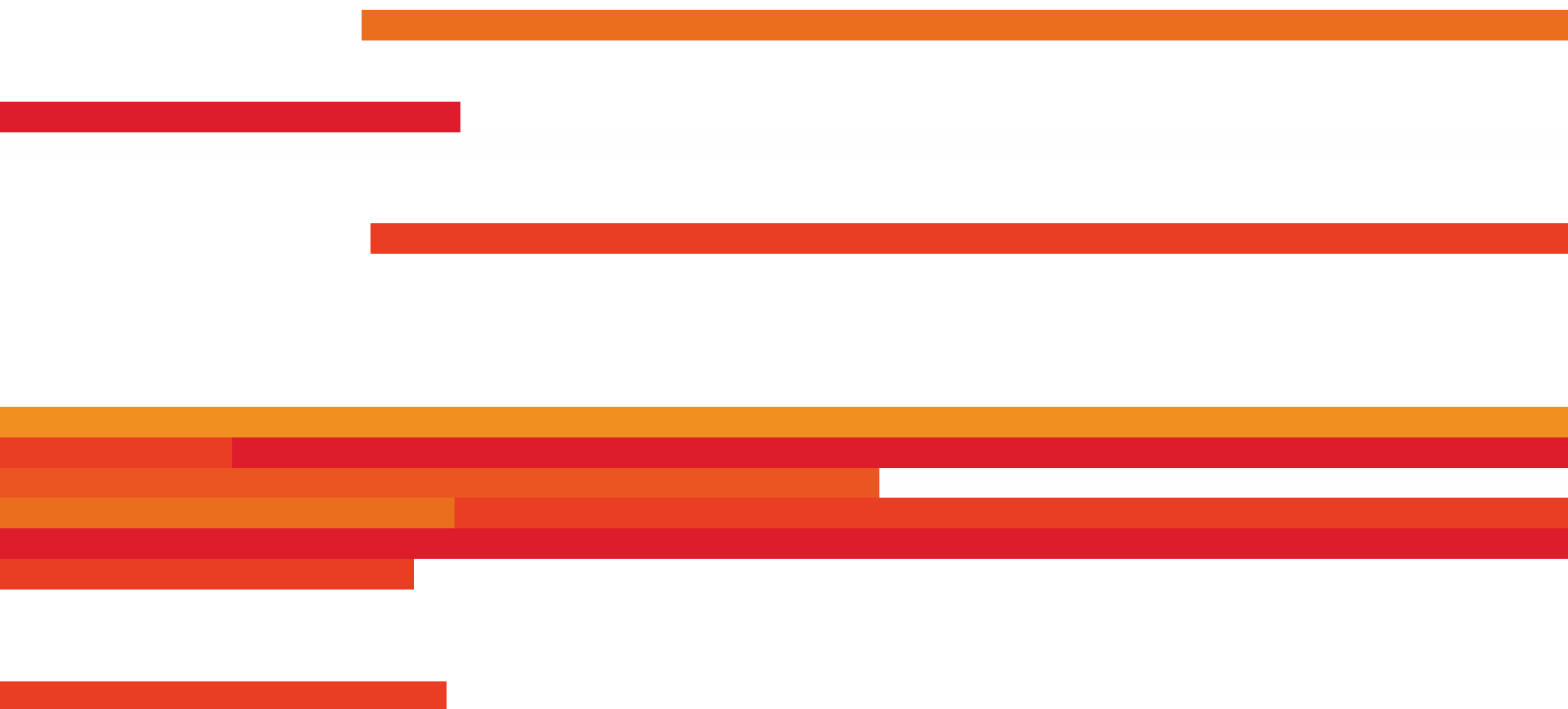




MISRA AC SLSF:2023 Amendment 1

Revisions for MATLAB Release R2023b

February 2024





First published February 2024 by The MISRA Consortium Limited
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ISBN 978-1-911700-12-8 PDF

British Library Cataloguing in Publication Data

A catalogue record for this book is available from the British Library.

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MISRA Mission Statement

MISRA provides world-leading best practice guidelines for the safe and secure application of both embedded control systems and standalone software.

MISRA is a collaboration between manufacturers, component suppliers, engineering consultancies and academics which seeks to research and promote best practice in developing safety- and security-related electronic systems and other software-intensive applications.

To this end, MISRA conducts research projects and publishes documents that provide accessible information for engineers and management.

MISRA also facilitates the dissemination and exchange of information between practitioners through supporting and holding technical events.

Disclaimer

Compliance with these guidelines does not in itself ensure error-free robust software.

Compliance with the requirements of this document, or any other standard, does not of itself confer immunity from legal obligations.

Acknowledgements

The MISRA consortium would like to thank the following individuals for their significant contribution to the writing of this document:

John Botham	Ricardo UK
Bill Burdock	Formerly Jaguar Land Rover
Ivan Ellis	Jaguar Land Rover
Alasdair Mitchell	BorgWarner

The MISRA consortium also wishes to acknowledge contributions from the following individuals during the development and review process:

David Ward	HORIBA MIRA
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Contents

1	Introduction	1
2	References	2
	Appendix C: Simulink diagnostic configuration	3
	Appendix D: Simulink window appearance settings	7
	Appendix E: Allowable Simulink and Stateflow blocks	9

1 Introduction

The MISRA Autocode (AC) family of documents deals with the application of language subsets for automatic code generation purposes. MISRA AC SLSF, *Modelling design and style guidelines for the application of Simulink and Stateflow*, specifies modelling best practices intended to assist users of MathWorks' tools Simulink and Stateflow to produce high quality models using them.

Simulink and Stateflow receive updates as part of regular MathWorks MATLAB releases. The second edition of the guidelines, MISRA AC SLSF:2023 [1], takes account of MATLAB releases up to and including R2023a.

This Amendment to MISRA AC SLSF:2023 contains modifications that bring the guidelines up to date for MATLAB release R2023b. Specifically, this document contains updated versions of:

- Appendix C: Simulink diagnostic configuration
- Appendix D: Simulink window appearance settings
- Appendix E: Allowable Simulink and Stateflow blocks

When applying MISRA AC SLSF:2023 in developments using MATLAB release R2023b, the versions of the Appendices in this document should be used instead of those in MISRA AC SLSF:2023 and references in MISRA AC SLSF:2023 to Appendices C, D or E should be taken to refer to the versions in this document.

2 References

- [1] MISRA AC SLSF:2023 *Modelling design and style guidelines for the application of Simulink and Stateflow*, ISBN 978-1-911700-07-4 (paperback), ISBN 978-1-911700-06-7 (PDF), The MISRA Consortium Limited, 2023
- [2] MISRA AC GMG:2023 *Generic modelling design and style guidelines*, ISBN 978-1-906400-04-3 (paperback), ISBN 978-1-906400-05-0 (PDF), The MISRA Consortium Limited, 2023

Appendix C: Simulink diagnostic configuration

The following main menu options shall be set for “diagnostic configuration” in the Simulink window on completion of the model¹:

Menu options	Related rule	Setting	Releases
Diagnostics / Solver			
Algebraic loop	None	Error	All
Minimize algebraic loop	None	Warning	All
Block priority violation	None	Error	All
Minimum step size violation	None	Error	All
Consecutive zero crossings violation	None	Error	All
Automatic solver parameter selection	MISRA AC SLSF 003 A	Error	All
Extraneous discrete derivative signals	None	Error	All
State name clash	MISRA AC GMG 006 C	Warning	All
Operating point restore interface checksum mismatch	None	Warning	2019A -
Sample hit time adjusting	None	Error	- 2015B
Unspecified inheritability of sample time	MISRA AC SLSF 009 C	Error	- 2015B
Solver data inconsistency	None	Warning	- 2015B
SimState interface checksum mismatch	None	Warning	- 2018B
SimState object from other release	None	Warning	- 2018B
Diagnostics / Advanced			
Allow symbolic dimension specification	None	Deselect	All
Check undefined subsystem initial output	MISRA AC SLSF 007 A	Selected	2014A -
Allow automatic unit conversions	None	Deselect	All
Allowed unit systems	None	SI Only	All
Units inconsistency messages	None	Warning	All
Solver data inconsistency	None	Warning	2017B -
Ignored zero crossings	None	Warning	2020A -
Masked zero crossings	None	Warning	2020A -
Initial state is array	None	Warning	All
Insufficient maximum identifier length	None	Warning	2020B -
Block diagram contains disabled library links	None	Error	2017B -
Block diagram contains parameterized library links	None	Error	2017B -
Combine output and update methods for code generation and simulation	None	Selected	All
Behavior when pregenerated library subsystem code is missing	None	Error	2022B -
Behavior when a matching unit test for subsystem reference is missing	None	Error	2023A -

¹ The grouping of the menu options varies between the different releases of Simulink. The options listed in the table may not be seen within the root of the menu but may be located at the second or third level of menu.

Menu options	Related rule	Setting	Releases
Enable debug execution mode for FMU Import blocks	None	Unselected	2019A -
Arithmetic operations in variant conditions	None	Error	2019A -
Variant condition mismatch at signal source and destination	None	Error	2020B -
Variant activation time inherited from Simulink.VariantControl	None	Error	2022B -
Variant configuration not used by top model	None	Error	2022B -
Check runtime output of execution context	None	Selected	- 2020B
Check undefined subsystem initial output	None	Selected	- 2022A
Diagnostics / Sample Time			
Source block specifies -1 sample time	MISRA AC SLSF 009 D	Error	All
Discrete used as continuous	MISRA AC SLSF 003 A	Error	- 2013B
Multitask rate transition	None	Error	- 2020A
Multitask data transfer	None	Error	2020B -
Single rate transition	None	Error	- 2020A
Single task data transfer	None	Error	2020B -
Multitask conditionally executed subsystem	None	Error	All
Tasks with equal priority	None	Error	All
Exported tasks rate transition	None	Error	2019B -
Enforce sample times specified by Signal Specification blocks	MISRA AC SLSF 009 D	Error	All
Sample hit time adjusting	None	Warning	2017B -
Unspecified inheritability of sample time	MISRA AC SLSF 009 C	Error	2017B -
Diagnostics / Data Validity / Signals			
Signal resolution	None	Explicit only	All
Division by singular matrix	None	Error	All
Underspecified data types	MISRA AC GMG 010 A, B, C	Error	All
Inf or NaN block output	MISRA AC GMG 013 B	Error	All
"rt" prefix for identifiers	MISRA AC GMG 006 D	Error	All
Wrap on overflow	MISRA AC GMG 010 B	Error	2014A -
Saturate on overflow	MISRA AC GMG 013 A	Error	2014A -
Underspecified dimensions	None	Error	2018B -
Simulation range checking	None	Error	All
String truncation checking	None	Error	2018A -
Detect overflow	MISRA AC GMG 010 D	Error	- 2013B
Diagnostics / Data Validity / Parameters			
Detect downcast	MISRA AC GMG 010 D	Error	All
Detect underflow	MISRA AC GMG 010 D	Error	All
Detect loss of tunability	None	Warning	All
Detect overflow	None	Error	All
Detect precision loss	None	Warning	All
Diagnostics / Data Validity / Data Store Memory Blocks			
Detect read before write	MISRA AC SLSF 045 C	Enable all as errors	All

Menu options	Related rule	Setting	Releases
Detect write after read	MISRA AC SLSF 045 C	Enable all as errors	All
Detect write after write	MISRA AC SLSF 055 D	Enable all as errors	All
Multitask data store	MISRA AC SLSF 005 C Data store blocks not permitted	Error	All
Duplicate data store names	None	Warning	All
Diagnostics / Data Validity / Advanced Parameters			
Multiple driving blocks executing at the same time step	MISRA AC SLSF 005 C Merge block not permitted	Error	All
Underspecified initialization detection	MISRA AC SLSF 007 A	Classic	All
Array bounds exceeded	MISRA AC GMG 010 D	Error	All
Model Verification block enabling	None	Use local settings	All
Detect ambiguous custom storage class final values	None	Error	- 2022A
Detect non-reused custom storage classes	None	Error	- 2022A
Parameter Writer block validation	None	Use local settings	2023A -
Diagnostics / Type Conversion			
Unnecessary type conversions	None	Warning	All
Vector/matrix block input conversion	MISRA AC SLSF 015 C	Error	All
32-bit integer to single precision float conversion	MISRA AC GMG 010 A	Warning	All
Detect underflow	MISRA AC GMG 010 D	Error	2013B -
Detect precision loss	MISRA AC GMG 010 D	Error	2013B -
Detect overflow	MISRA AC GMG 010 D	Error	All
Diagnostics / Connectivity / Signals			
Signal label mismatch	MISRA AC SLSF 027 B, C, D, E	Error	All
Unconnected block input ports	MISRA AC SLSF 017 A	Error	All
Unconnected block output ports	MISRA AC SLSF 017 A	Error	All
Unconnected line	MISRA AC SLSF 017 B	Error	All
Diagnostics / Connectivity / Buses			
Unspecified bus object at root Output block	None	Error	All
Element name mismatch	MISRA AC SLSF 027 B	Error	All
Bus signal treated as vector	MISRA AC SLSF 016 D	Error	All
Non-bus signals treated as bus signals	MISRA AC SLSF 016 D, E	Error	2013B -
Repair bus selections	None	Warn and repair	2009B -
Mux blocks used to create bus signals	MISRA AC SLSF 016 A	Error	- 2015B
Diagnostics / Connectivity / Function calls			
Context-dependent inputs	None	Error	All
Invalid function-call connection	None	Error	- 2019B
Diagnostics / Compatibility			
S-function upgrades needed	None	Warning	All

Menu options	Related rule	Setting	Releases
Block behaviour depends on frame status of signal	None	Error	2009B -
Operating point object from earlier release	None	Error	2021B -
SimState object from earlier release	None	Warning	- 2021A
Diagnostics / Model Referencing			
Model block version mismatch	MISRA AC GMG 001 A	Warning	All
Port and parameter mismatch	None	Warning	All
Invalid root Inport/Outport block connection	None	Warning	All
Unsupported data logging	MISRA AC SLSF 005 A To File & To Workspace blocks not permitted	Error	All
No explicit final value for model arguments	None	Warning	2020B -
Model Configuration mismatch	Section 3.2 Model configuration	Warning	- 2013B
Diagnostics / Saving			
Block diagram contains disabled library links	None	Error	- 2015B
Block diagram contains parameterized library links	None	Error	- 2015B
Diagnostics / Stateflow			
Unused data, events, messages and functions	MISRA AC SLSF 037 G	Error	2013B -
Unexpected backtracking	MISRA AC SLSF 043 I	Error	2013B -
Invalid input data access in chart initialization	None	Warning	2009B -
No unconditional default transitions	MISRA AC SLSF 042 D	Error	2013B -
Transition outside natural parent	MISRA AC SLSF 034 C	Warning	2013B -
Undirected event broadcasts	MISRA AC SLSF 047 A	Error	2013B -
Transition action specified before condition action	MISRA AC SLSF 045 C	Error	2013B -
Read-before-write to output in Moore chart	None	Error	2014A -
Absolute time temporal value shorter than sampling period	None	Warning	2017B -
Self transition on leaf state	None	Warning	2017B -
Execute-at-Initialization disabled in presence of input events	None	Warning	2017B -
Unreachable execution path	MISRA AC SLSF 043 C, E, F	Error	2017B -
Transition shadowing	MISRA AC SLSF 043 I	Error	2013B - 2015B
Diagnostics / Stateflow / Advanced Parameters			
Use of machine-parented data instead of Data Store Memory	None	Error	2017B - 2022B

Appendix D: Simulink window appearance settings

The following appearance options must be set in the Simulink window on completion of the model²:

Menu options	Related rule	Setting
Model Display		
Model Browser	None	Selected
Status bar	None	Selected
Toolstrip ^{*1}	None	Selected
Explorer bar	None	Selected
Background ^{*2}	MISRA AC SLSF 019	White
Zoom	MISRA AC SLSF 019	100%
Show Markup	None	Selected
Smart guides	None	Selected
Sample Time		
Colours	None	Deselected
Text	None	Deselected
Timing Legend	None	Deselected
Automatic Rate Transitions	MISRA AC SLSF 058	Deselected
Block Display		
Show block name	MISRA AC SLSF 026	As per related rule
Foreground Colour ^{*3}	MISRA AC SLSF 023	Black
Background Colour ^{*3}	MISRA AC SLSF 023	White
Content Preview	None	Deselected
Port Labels	None	From Port Block Name
Hide Automatic Block Names	MISRA AC SLSF 026	Deselected
Show Block Name	MISRA AC SLSF 026	Deselected
Library links	None	Show All Links
Execution order ^{*4}	None	Deselected
Connectors	None	Deselected
Reduced Blocks	None	Deselected
Variant conditions	None	Deselected
Variant Legend	None	Deselected
Variant Fading	None	Deselected
Reference Model I/O mismatch	None	Deselected
Reference Model version	None	Deselected
Name in Tooltip	None	Deselected
Description in Tooltip	None	Deselected
Parameters in Tooltip	None	Deselected
Signal Display		
Signal dimensions	None	Deselected
Nonscalar signals ^{*5}	None	Selected

² The location of the options listed in the table varies between the different releases of Simulink. In the latest versions they are spread across different toolbars and the right-click menu on a selected block.

Menu options	Related rule	Setting
Signal Data Ranges	None	Deselected
Propagated signal labels	None	Deselected
Signal resolves to object	None	Deselected
Storage class	None	Deselected
Ports		
Port Data Types	None	Deselected
Port units	None	Deselected
Alias Data Types	None	Deselected
Port Number	None	Deselected
Signal Badges		
Test Point * ⁶	None	Selected
Logging & Viewers * ⁷	None	Selected
Linearization indicators	None	Selected
Execution context indicator	None	Deselected

Notes:

Key	Description
* ¹	This menu option was renamed from Toolbars.
* ²	This menu option was renamed from Canvas Colour.
* ³	These menu options were renamed from Block Colours.
* ⁴	This menu option was renamed from Sorted execution order.
* ⁵	This menu option was renamed from Wide non-scalar lines.
* ⁶	This menu option was renamed from Testpoint and logging indicators.
* ⁷	This menu option was renamed from Viewer indicator.

Appendix E: Allowable Simulink and Stateflow blocks

The table below details which Simulink and Stateflow blocks from The MathWorks libraries are allowed in controller and plant modelling. The criteria for allowable blocks are detailed in rule MISRA AC SLSF 005.

A detailed description of each block can be found in documentation from The MathWorks and can be used in conjunction with the criteria for allowable blocks to understand its assigned status.

Additional information for some blocks is provided in the notes section at the end of the table.

Status Key	N	Not allowed for controller or plant modelling
	P	Allowed for plant modelling only
	A	Allowed block (controller and plant)

Category	Block	Show block name	Can be re-sized	Status	Releases
Additional Math and Discrete	Fixed-Point State-Space			N	R2009A -
	Transfer FcnDirect Form II			N	R2009A -
	Transfer FcnDirect Form II Time Varying			N	R2009A -
	Unit Delay Enabled			N	R2009A - R2016A
	Unit Delay Enabled External IC			N	R2009A - R2016A
	Unit Delay Enabled Resettable			N	R2009A - R2016A
	Unit Delay Enabled Resettable External IC			N	R2009A - R2016A
	Unit Delay External IC			N	R2009A - R2016A
	Unit Delay Resettable			N	R2009A - R2016A
	Unit Delay Resettable External IC			N	R2009A - R2016A
	Unit Delay With Preview Enabled			N	R2009A - R2016A
	Unit Delay With Preview Enabled Resettable			N	R2009A - R2016A
	Unit Delay With Preview Enabled Resettable External RV			N	R2009A - R2016A
	Unit Delay With Preview Resettable			N	R2009A - R2016A
	Unit Delay With Preview Resettable External RV			N	R2009A - R2016A
	Decrement Real World			N	R2009A -
	Decrement Stored Integer			N	R2009A -
	Decrement To Zero			N	R2009A -
	Decrement Time To Zero			N	R2009A -
	Increment Real World			N	R2009A -
Increment Stored Integer			N	R2009A -	
Continuous	Derivative * ¹	N	N	P	R2009A -
	Descriptor State-Space			N	R2018B -
	Entity Transport Delay			N	R2019B -
	First Order Hold			N	R2019B -
	Integrator * ²	N	N	P	R2009A -
	Integrator Limited			N	R2009A -
	Integrator, Second-Order			N	R2010A -
	Integrator, Second-Order Limited			N	R2010A -
	PID Controller			N	R2009B -
	PID Controller (2DOF)			N	R2009B -
	State-Space	Y	N	P	R2009A -
	Transfer Fcn	Y	N	P	R2009A -
	Transport Delay	Y	N	P	R2009A -
	Variable Time Delay	Y	N	P	R2009A -
	Variable Transport Delay			N	R2009A -
Zero-Pole	Y	N	P	R2009A -	
Discontinuities	Backlash	Y	N	P	R2009A -
	Coulomb and Viscous Friction	Y	N	P	R2009A -
	Dead Zone	Y	N	P	R2009A -
	Dead Zone Dynamic			N	R2009A -
	Hit Crossing			N	R2009A -
	PWM			N	R2020B -

Category	Block	Show block name	Can be re-sized	Status	Releases
	Quantizer	Y	N	A	R2009A -
	Rate Limiter			N	R2009A -
	Rate Limiter Dynamic			N	R2009A -
	Relay	Y	N	A	R2009A -
	Saturation	Y	N	A	R2009A -
	Saturation Dynamic			N	R2009A -
	Variable Pulse Generator			N	R2020B -
	Wrap To Zero			N	R2009A -
Discrete	Delay			N	R2009A -
	Difference			N	R2009A -
	Discrete Transfer Fcn	Y	N	A	R2009A -
	Discrete Zero-Pole			N	R2009A -
	Discrete Derivative			N	R2009A -
	Discrete FIR Filter			N	R2009A -
	Discrete Filter			N	R2009A -
	Discrete PID Controller			N	R2009B -
	Discrete PID Controller (2DOF)			N	R2009B -
	Discrete State-Space	Y	N	A	R2009A -
	Discrete-Time Integrator			N	R2009A -
	Enabled Delay			N	R2015A - R2017A
	First-Order Hold			N	R2009A -
	Memory	Y	N	P	R2009A -
	Propagation Delay			N	R2022B -
	Resettable Delay			N	R2012B -
	Tapped Delay			N	R2009A -
	Transfer Fcn First Order			N	R2009A -
	Transfer Fcn Lead or Lag			N	R2009A -
	Transfer Fcn Real Zero			N	R2009A -
Unit Delay	Y	N	A	R2009A -	
Variable Integer Delay			N	R2012B -	
Zero-Order Hold	Y	N	A	R2009A -	
Logic and Bit Operations	Bit Clear			N	R2009A -
	Bit Set			N	R2009A -
	Bit to Integer Converter			N	R2022A -
	Bitwise Operator	Y	N	A	R2009A -
	Combinatorial Logic			N	R2009A -
	Compare To Constant			N	R2009A -
	Compare To Zero			N	R2009A -
	Detect Change			N	R2009A -
	Detect Decrease			N	R2009A -
	Detect Increase			N	R2009A -
	Detect Fall Negative			N	R2009A -
	Detect Fall Nonpositive			N	R2009A -
	Detect Rise Nonnegative			N	R2009A -
	Detect Rise Positive			N	R2009A -
	Extract Bits			N	R2009A -
	Float Extract Bits			N	R2023A -
	Integer to Bit Converter			N	R2022A -
	Interval Test			N	R2009A -
	Interval Test Dynamic			N	R2009A -
	Logical Operator	N	Y	A	R2009A -
Relational Operator	N	N	A	R2009A -	
Shift Arithmetic	Y	N	A	R2009A -	
Lookup Tables	1-D Lookup Table	Y	N	A	R2009A -
	2-D Lookup Table	Y	N	A	R2009A -
	Cosine	Y	N	A	R2009A -
	Direct Lookup Table (n-D)	Y	N	A	R2009A -
	Interpolation Using Prelookup	Y	N	A	R2009A -
	Lookup Table Dynamic	N	N	A	R2009A -
	Prelookup	Y	N	A	R2009A -
	Sine			N	R2009A -
n-D Lookup Table			N	R2009A -	

Category	Block	Show block name	Can be re-sized	Status	Releases
Math Operations	Abs	N	N	A	R2009A -
	Add	N	Y	A	R2009A -
	Algebraic Constraint			N	R2009A -
	Assignment			N	R2009A -
	Bias			N	R2009A -
	Complex to Magnitude-Angle	N	N	A	R2009A -
	Complex to Real-Imag	N	N	A	R2009A -
	Divide			N	R2009A -
	Dot Product	N	N	A	R2009A -
	Find Nonzero Elements			N	R2009A -
	Gain	Y	N	A	R2009A -
	Magnitude-Angle to Complex	N	N	A	R2009A -
	Math Function	N	N	A	R2009A -
	Matrix Concatenate			N	R2009A -
	Min Max	N	Y	A	R2009A -
	Min Max Running Resettable			N	R2009A -
	Permute Dimensions	Y	N	A	R2009A -
	Polynomial	Y	N	A	R2009A -
	Product	N	Y	A	R2009A -
	Product of Elements			N	R2009A -
	Real-Imag to Complex	N	N	A	R2009A -
	Reciprocal Sqrt			N	R2009A - R2019A
	Reshape	N	N	A	R2009A -
	Rounding Function			N	R2009A -
	Sign			N	R2009A -
	Signed Sqrt			N	R2010A - R2018A
	Sine Wave Function			N	R2009A -
	Slider Gain	Y	N	P	R2009A -
	Sqrt	N	N	P	R2010A -
	Squeeze			N	R2009A -
	Subtract			N	R2009A - R2018A
	Sum	Y	Y	A	R2009A - R2018A
	Sum of Elements	Y	Y	A	R2009A - R2018A
	Trigonometric Function	N	N	P	R2009A -
Unary Minus			N	R2009A -	
Vector Concatenate			N	R2009A -	
Weighted Sample Time Math			N	R2009A -	
Model Verification	Assertion			N	R2009A -
	Check Discrete Gradient			N	R2009A -
	Check Dynamic Gap			N	R2009A -
	Check Dynamic Range			N	R2009A -
	Check Static Gap			N	R2009A -
	Check Static Range			N	R2009A -
	Check Dynamic Lower Bound			N	R2009A -
	Check Dynamic Upper Bound			N	R2009A -
	Check Input Resolution			N	R2009A -
	Check Static Lower Bound			N	R2009A -
Check Static Upper Bound			N	R2009A -	
Model-Wide Utilities	Block Support Table			N	R2009A -
	DocBlock	N	N	A	R2009A -
	Hit Scheduler			N	R2022B -
	Model Info	N	Y	A	R2009A -
	Timed-Based Linearization			N	R2009A -
	Trigger-Based Linearization			N	R2009A -
Ports and Subsystems	Inport	Y	N	A	R2009A -
	Enable	N	N	A	R2009A -
	Trigger	N	N	A	R2009A -
	Atomic Subsystem			N	R2009A - R2018A
	Code Reuse Subsystem			N	R2009A - R2018A
	Configurable Subsystem			N	R2009A -
	Enabled Subsystem			N	R2009A -

Category	Block	Show block name	Can be re-sized	Status	Releases
	Enabled and Triggered Subsystem			N	R2009A -
	For Each Subsystem			N	R2009A -
	For Iterator Subsystem			N	R2009A -
	Function-Call Feedback Latch			N	R2009A -
	Function-Call Generator	N	N	A	R2009A -
	Function-Call Split			N	R2010A -
	Function-Call Subsystem			N	R2009A -
	Function Element			N	R2022A -
	Function Element Call			N	R2022A -
	If			N	R2009A -
	If Action Subsystem			N	R2009A -
	In Bus Element			N	R2017A -
	Model	Y	Y	A	R2009A -
	Model Variants			N	R2009B - R2018A
	Neighborhood Processing Subsystem			N	R2022B -
	Out Bus Element			N	R2017A -
	Resettable Subsystem	Y	Y	A	R2015A -
	Subsystem	Y	Y	A	R2009A -
	Subsystem Examples			N	R2009A - R2017A
	Switch Case			N	R2009A -
	Switch Case Action Subsystem			N	R2009A -
	Triggered Subsystem			N	R2009A -
	Unit System Configuration			N	R2016A -
	Variant Subsystem			N	R2010B -
	While Iterator Subsystem			N	R2009A -
	Outport	Y	N	A	R2009A -
Signal Attributes	Bus to Vector			N	R2009A -
	Data Type Conversion	N	N	A	R2009A -
	Data Type Conversion Inherited			N	R2009A -
	Data Type Duplicate			N	R2009A -
	Data Type Propagation			N	R2009A -
	Data Type Propagation Examples			N	R2009A - R2017A
	Data Type Scaling Strip			N	R2009A -
	IC			N	R2009A -
	Probe			N	R2009A -
	Rate Transition			N	R2009A -
	Signal Conversion			N	R2009A -
	Signal Specification	Y	N	A	R2009A -
	Unit Conversion			N	R2016A -
	Weighted Sample Time			N	R2009A -
	Width	N	N	A	R2009A -
Signal Routing	Bus Element In			N	R2017A
	Bus Element Out			N	R2017A
	Bus Assignment	N	Y	A	R2009A -
	Bus Creator	N	Y	A	R2009A -
	Bus Selector	N	Y	A	R2009A -
	Data Store Memory			N	R2009A -
	Data Store Read			N	R2009A -
	Data Store Write			N	R2009A -
	Demux	N	Y	A	R2009A -
	Environment Controller			N	R2009A -
	From	N	Y	A	R2009A -
	Goto	N	Y	A	R2009A -
	Goto Tag Visibility			N	R2009A -
	Index Vector			N	R2009A -
	Manual Switch			N	R2009A -
	Manual Variant Sink			N	R2016B -
	Manual Variant Source			N	R2016B -
	Merge			N	R2009A -
	Multiport Switch	N	Y	A	R2009A -
	Mux	N	Y	A	R2009A -
	Parameter Writer			N	R2019A -

Category	Block	Show block name	Can be re-sized	Status	Releases
	Selector	Y	N	A	R2009A -
	State Reader			N	R2016B -
	State Writer			N	R2016B -
	Switch	N	N	A	R2009A -
	Variant Sink			N	R2016B -
	Variant Source			N	R2016B -
	Vector Concatenate	N	Y	A	R2009B -
Sinks	Display			N	R2009A -
	Floating Scope			N	R2009A -
	Out Bus Element			N	R2017A -
	Scope			N	R2009A -
	Stop Simulation			N	R2009A -
	Terminator	N	N	A	R2009A -
	To File			N	R2009A -
	To Workspace			N	R2009A -
	XY Graph			N	R2009A -
	Outport	Y	N	A	R2009A -
Sources	Inport	Y	N	A	R2009A -
	Band-Limited White Noise	Y	N	P	R2009A -
	Chirp Signal	Y	N	P	R2009A -
	Clock	N	N	P	R2009A -
	Constant	Y	N	A	R2009A -
	Counter Free-Running			N	R2009A -
	Counter Limited	Y	N	P	R2009A -
	Digital Clock	Y	N	P	R2009A -
	Enumerated Constant			N	R2009B -
	From Workspace	Y	N	P	R2009A -
	From Spreadsheet	Y	N	P	R2017B -
	From File	Y	N	P	R2009A -
	Ground * ³	N	N	P	R2009A -
	In Bus Element			N	R2017A -
	Playback	N	N	P	R2022b -
	Pulse Generator	Y	N	P	R2009A -
	Ramp	Y	N	P	R2009A -
	Random Number	Y	N	P	R2009A -
	Repeating Sequence	Y	N	P	R2009A -
	Repeating Sequence Interpolated	Y	N	P	R2009A -
	Repeating Sequence Stair	Y	N	P	R2009A -
	Signal Editor	Y	N	P	R2017B -
	Signal Generator	Y	N	P	R2009A -
	Signal Builder	Y	N	P	R2009A -
	Sine Wave	Y	N	P	R2009A -
	Step	Y	N	P	R2009A -
	Uniform Random Number	Y	N	P	R2009A -
Waveform Generator			N	R2014A -	
User-Defined Functions	C Caller			N	R2018B -
	C Function			N	R2020A -
	Fcn			N	R2009A - R2020A
	Function Caller			N	R2014B -
	Initialize Function			N	R2016B -
	Interpreted MATLAB Function			N	R2009A -
	Level-2 MATLAB S-Function	Y	Y	P	R2009A -
	MATLAB Function	Y	Y	P	R2009A -
	MATLAB System			N	R2013B -
	Reinitialize Function			N	R2022A -
	Reset Function			N	R2017B -
	S-Function	Y	Y	A	R2009A -
	S-Function Builder	Y	Y	P	R2009A -
	S-Function Examples			N	R2009A -
	Simulink Function			N	R2014B -
Terminate Function			N	R2016B -	

Category	Block	Show block name	Can be re-sized	Status	Releases
Stateflow	Chart	Y	Y	A	R2009A -
	Chart (MATLAB)			N	R2012B - R2013B
	Message Viewer			N	R2015B - R2017A
	Sequence Viewer			N	R2017B -
	State Transition Table			N	R2012B -
	Truth Table			N	R2009A -

Notes:

Key	Description
*1	Use of this block may impact simulation times, depending on the solver and its settings.
*2	The accuracy of this block's output may depend on the chosen solver.
*3	Use of this block may mask errors, and in some circumstances it will not output the expected value.